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# SPECIFICATIONS

Product Name     Multi-layer Chip Power Inductor       Sunlord Part Number     MCL1005N Series	Customer	
	Product Name	Multi-layer Chip Power Inductor
	Sunlord Part Number	MCL1005N Series
Customer Part Number	Customer Part Number	

## [⊠New Released, □Revised]

SPEC No.: MCL0103210000

Rev.	Effective Date	Changed Contents	Change Reasons	Approved By
01	/	New release	/	Hai Guo

[This SPEC is total 11 pages.]

[ROHS, Halogen-Free and SVHC Compliant Parts]

Approved By	Checked By	Issued By
戰	高簡	

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[For Customer appro	val Only】	Date:	
Qualification Status:	🗌 Full 🗌 Re	estricted 🛛 🗌 Rejec	ted
Approved By	Verified By	Re-checked By	Checked By
Comments:			

#### Caution

All products listed in this specification are developed, designed and intended for use in general electronics equipment. The products are not designed or warranted to meet the requirements of the applications listed below, whose performance and/or quality require especially high reliability, or whose failure, malfunction or trouble might directly cause damage to society, person, or property. Please understand that we are not responsible for any damage or liability caused by use of the products in any of the applications below. Please contact us for more details if you intend to use our products in the following applications.

- 1. Aircraft equipment
- 2. Aerospace equipment
- 3. Undersea equipment
- 4. nuclear control equipment
- 5. military equipment
- 6. Power plant equipment
- 7. Medical equipment
- 8. Transportation equipment (automobiles, trains, ships, etc.
- 9. Traffic signal equipment
- 10. Disaster prevention / crime prevention equipment
- 11. Data-processing equipment
- 12. Applications of similar complexity or with reliability requirements comparable to the applications listed in the above

#### 1. Scope

This specification applies to MCL1005N Series of multi-layer chip power inductors.

#### 2. Product Description and Identification (Part Number)

- 1) Description
  - MCL1005N Series of multi-layer chip power inductors
- 2) Product Identification (Part Number)
  - <u>MCL 1005 N R16 J T</u> ① ② ③ ④ ⑤ ⑥

<ol> <li>Type</li> </ol>	9
MCL	Monolithic Type Power Inductor

③Application Type	
Ν	Near Field Communication

⑤Inductance Tolerance		
J	±5%	
К	±10%	
М	±20%	

②External Dimensions (L x W) (mm)		
1005	1.0x0.5	

(4) Nominal Inductance	
Example	Nominal Value
R16	0.16µH
R56	0.56µH

6 Packing	
Т	Tape Carrier Package

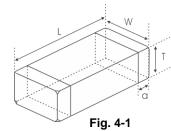
#### 3. Electrical Characteristics

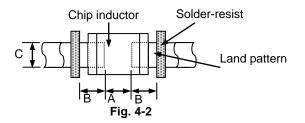
Please refer to Appendix A (Page 9).

- 1) Operating and storage temperature range (individual chip without packing): -40  $^\circ$ C ~ +85  $^\circ$ C
- 2) Storage temperature range (packaging conditions): -10°C ~+40°C and RH 70% (Max.)

#### 4. Shape and Dimensions

- 1) Dimensions and recommended PCB pattern for reflow soldering: See Fig.4-1, Fig.4-2 and Table 4-1.
- 2) Structure: See Fig. 4-3 and Fig. 4-4.

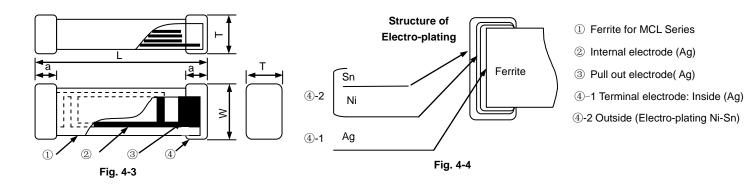




[Table 4-1]

Unit: mm [inch]

Туре	L	W	Т	а	A	В	С
1005 [0402]	1.0±0.15 [0.039±0.006]	0.5±0.15 [0.020±0.006]	0.5±0.15 [0.020±0.006]	0.25±0.1 [0.010±0.004]	0.45~0.55	0.40~0.50	0.45~0.55



#### Sunlord Categories: general confidential

3) Material Information: See Table 4-2.

[Table 4-2]

Code	Part Name	Material Name
1	Ferrite Body	Ferrite Powder
2	Inner Coils	Silver Paste
3	Pull-out Electrode (Ag)	Silver Paste
<b>④-1</b>	Terminal Electrode: Inside Ag	Termination Silver Composition
<b>④-2</b>	Electro-Plating: Ni/Sn plating	Plating Chemicals

#### 5. Test and Measurement Procedures

#### 5.1 Test Conditions

Unless otherwise specified, the standard atmospheric conditions for measurement/test as:

- a. Ambient Temperature: 20±15°C
- b. Relative Humidity: 65±20%
- c. Air Pressure: 86kPa to 106kPa
- If any doubt on the results, measurements/tests should be made within the following limits:
  - a. Ambient Temperature: 20±2°C
  - b. Relative Humidity: 65±5%
  - c. Air Pressure: 86kPa to 106kPa

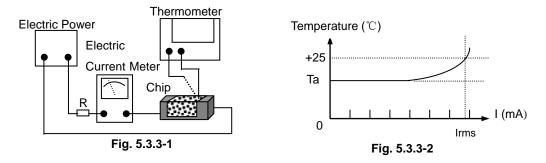
#### 5.2 Visual Examination

a. Inspection Equipment: 20× magnifier

#### 5.3 Electrical Test

- 5.3.1 DC Resistance (DCR)
  - a. Refer to Appendix A.
  - b. Test equipment (Analyzer): High Accuracy Milliohmmeter-HP4338B or equivalent.
- 5.3.2 Inductance (L)
  - a. Refer to Appendix A.
  - b. Test equipment: High Accuracy RF Impedance /Material Analyzer-HP4291B+HP16192A or equivalent.
  - c. Test signal: -20dBm or 50mV.
  - d. Test frequency refers to Appendix A.
- 5.3.3 Temperature Rise Current (Irms)
  - a. Refer to Appendix A.
  - b. Test equipment (see Fig. 5.3.3-1): Electric Power, Electric current meter, Thermometer.
  - c. Measurement method (see Fig. 5.3.3-1):
    - 1. Set test current to be 0mA.
    - 2. Measure initial temperature of chip surface.
    - 3. Gradually increase voltage and measure chip temperature for corresponding current.

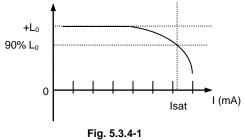
4. Definition of Temperature Rise Current (Irms) : Irms is direct electric current as chip surface temperature rose just25°C against chip initial surface temperature (Ta) (see Fig. 5.3.3-2)



5.3.4 Saturation Current (Isat)

- a. Refer to Appendix A.
- b. Test equipment: HP6632B system DC power supply, HP4291B+HP16192A+HP16200A or equivalent.
- c. Measurement method:
  - 1. Measurement conditions of initial inductance L: Measuring Frequency: 25MHz.
    - Test Current: 1mA.

2. Definition of Saturation Current(Isat): Isat is the value of DC current as inductance L (nH) decreased just 10% against initial value (see Fig. 5.3.4-1).



5.3.5Self-Resonant Frequency (SRF)

- a. Refer to Appendix A.
- Test equipment: High Accuracy RF Impedance /Material Analyzer-HP4291B+HP16192A or equivalent. b.
- Test signal: -20dBm or 50 mV. c.

5.4 Reliabili		
Items	Requirements	Test Methods and Remarks
5.4.1 Terminal Strength	No removal or split of the termination or other defects shall occur. Chip F Mounting Pad Glass Epoxy Board Fig.5.4.1-1	<ol> <li>Solder the inductor to the testing jig (glass epoxy board shown in Fig.5.4.1-1) using eutectic solder. Then apply a 10N force in the direction of the arrow.</li> <li>Keep time: 10±1s.</li> <li>Speed: 1.0mm/s.</li> </ol>
5.4.0	-	
5.4.2 Resistance to Flexure	No visible mechanical damage. Unit: mm [inch]	<ul> <li>Solder the inductor to the test jig (glass epoxy board shown in Fig.5.4.2-1) Using a eutectic solder. Then apply a force in the direction shown Fig. 5.4.2-2.</li> <li>Flexure: 2mm.</li> <li>Pressurizing Speed: 0.5mm/sec.</li> <li>Keep time: 30 sec.</li> </ul>
5.4.3 Vibration	<ol> <li>No visible mechanical damage.</li> <li>Inductance change: Within ±5%.</li> <li>Cu pad Solder mask</li> <li>Glass Epoxy Board</li> <li>Fig. 5.4.3-1</li> </ol>	<ol> <li>Solder the inductor to the testing jig (glass epoxy board shown in Fig.5.4.3-1) using eutectic solder.</li> <li>The inductor shall be subjected to a simple harmonic motion having total amplitude of 1.5mm, the frequency being varied uniformly between the approximate limits of 10 and 55 Hz.</li> <li>The frequency range from 10 to 55 Hz and return to 10 Hz shall be traversed in approximately 1 minute. This motion shall be applied for a period of 2 hours in each 3mutually perpendicular directions (total of 6 hours).</li> </ol>
5.4.4	<ol> <li>No visible mechanical damage.</li> </ol>	Drop chip inductor 10 times on a concrete floor from a height of
Dropping	② Inductance change: Within ±5%.	100 cm.
5.4.5 Temperature	Inductance change should be within ±5% of initial value measuring at 20°C.	Temperature range: -40°C~ +85°C Reference temperature: +20°C
5.4.6 Solderability	<ol> <li>No visible mechanical damage.</li> <li>Wetting shall exceed 95% coverage.</li> </ol>	<ol> <li>Solder temperature: 240±2℃</li> <li>Duration: 3 sec.</li> <li>Solder: Sn/3.0Ag/0.5Cu.</li> <li>Flux: 25% Resin and 75% ethanol in weight.</li> </ol>
5.4.7 Resistance to Soldering Heat	<ol> <li>No visible mechanical damage.</li> <li>Wetting shall exceed 95% coverage.</li> <li>Inductance change: Within ±5%.</li> </ol>	<ol> <li>Solder temperature: 260±3°C.</li> <li>Duration: 5 sec.</li> <li>Solder: Sn/3.0Ag/0.5Cu.</li> <li>Flux: 25% Resin and 75% ethanol in weight.</li> <li>The chip shall be stabilized at normal condition for 1~2 hours before measuring.</li> </ol>

Categories: general confidential

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Items	Requirements	Test Methods and Remarks		
5.4.8 Thermal Shock	①       No mechanical damage.         ②       Inductance change: Within ±5%.         30 min.       30 min.         Ambient	<ol> <li>Temperature, Time: (See Fig.5.4.8-1) -40°C for 30±3 min→ 85°C for 30±3min.</li> <li>Transforming interval: 20 sec.(max.).</li> <li>Tested cycle: 100 cycles.</li> <li>The chip shall be stabilized at normal condition for 1~2 hours before measuring.</li> </ol>		
5.4.9 Resistance to Low Temperature	<ol> <li>No mechanical damage.</li> <li>Inductance change: Within ±5%.</li> </ol>	<ol> <li>Temperature: -40±2℃</li> <li>Duration: 1000<sup>+24</sup> hours.</li> <li>The chip shall be stabilized at normal condition for 1~2 hours before measuring.</li> </ol>		
5.4.10 Resistance to High Temperature	<ol> <li>No mechanical damage.</li> <li>Inductance change: Within ±5%.</li> </ol>	<ol> <li>Temperature: 85±2°C</li> <li>Duration: 1000<sup>+24</sup> hours.</li> <li>The chip shall be stabilized at normal condition for 1~2 hours before measuring.</li> </ol>		
5.4.11 Damp Heat (Steady States)	<ol> <li>No visible mechanical damage.</li> <li>Inductance change: Within ±5%.</li> </ol>	<ol> <li>Temperature: 60±2°C</li> <li>Humidity: 90% to 95% RH.</li> <li>Duration: 1000<sup>+24</sup> hours.</li> <li>The chip shall be stabilized at normal condition for 1~2 hours before measuring.</li> </ol>		
5.4.12 Loading Under Damp Heat	<ol> <li>No visible mechanical damage.</li> <li>Impedance change: within ±5%.</li> </ol>	<ol> <li>Temperature: 60±2°C.</li> <li>Humidity: 90% to 95% RH.</li> <li>Duration: 1000<sup>+24</sup> hours.</li> <li>Applied current: Rated current.</li> <li>The chip shall be stabilized at normal condition for 1~2 hours before measuring.</li> </ol>		
5.4.13 Loading at High Temperature (Life Test)	<ol> <li>No visible mechanical damage.</li> <li>Impedance change: within ±5%.</li> </ol>	<ol> <li>Temperature: 85±2°C</li> <li>Duration: 1000<sup>+24</sup> hours.</li> <li>Applied current: Rated current.</li> <li>The chip shall be stabilized at normal condition for 1~2 hours before measuring.</li> </ol>		

#### 6. Packaging and Storage

#### 6.1 Packaging

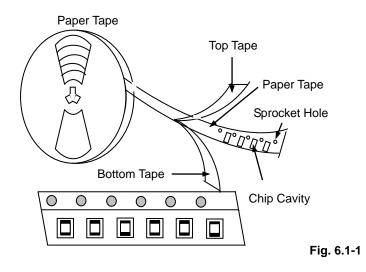
Tape Carrier Packaging:

Packaging code: T

- a. Tape carrier packaging are specified in attached figure Fig.6.1-1~3
- b. Tape carrier packaging quantity please see the following table:

Туре	1005[0402]		
T(mm)	0.5±0.15		
Таре	Paper Tape		
Quantity	10K		

- c. Reel shall be packaged in vinyl bag.
- d. Maximum of 5 or 10 reels bags shall be packaged in an inner box.
- e. Maximum of 6 or 10 inner boxes shall be packaged in an outer case.
- (1) Taping Drawings (Unit: mm)



**Remark:** The sprocket holes are to the right as the tape is pulled toward the user.

(2) Taping Dimensions (Unit: mm)

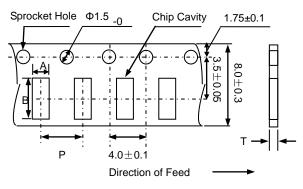
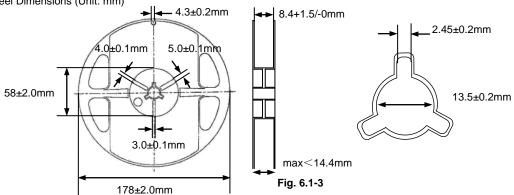


Fig. 6.1-2

(3) Reel Dimensions (Unit: mm)



#### 6.2 Storage

- a. The solderability of the external electrode may be deteriorated if packages are stored where they are exposed to high humidity. Package must be stored at 40°C or less and 70% RH or less.
- b. The solderability of the external electrode may be deteriorated if packages are stored where they are exposed to dust of harmful gas (e.g. HCl, sulfurous gas of H<sub>2</sub>S).
- c. Packaging material may be deformed if package are stored where they are exposed to heat of direct sunlight.
- d. Solderability specified in Clause 5.4.6 shall be guaranteed for 12 months from the date of delivery on condition that they are stored at the environment specified in Clause 3. For those parts, which passed more than 12 months shall be checked solder-ability before use.

Туре	А	В	Р	T max	
1005[0402]	0.65±0.1	1.15±0.1	2.0±0.05	0.8	

#### Sunlord Categories: general confidential

#### 7. Recommended Soldering Technologies

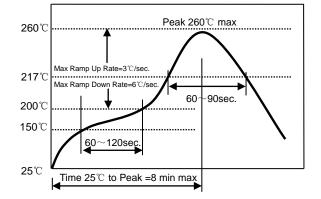
- 7.1 Reflowing Profile:
- $\bigtriangleup$  Preheat condition: 150 ~200  $^\circ\!\mathrm{C}/60$  ~120sec.
- $\bigtriangleup$   $\$  Allowed time above 217  $^\circ\!\mathrm{C}\colon$  60~90sec.
- $\triangle$  Max temp: 260°C
- $\triangle$  Max time at max temp: 10sec.
- △ Solder paste: Sn/3.0Ag/0.5Cu
- $\bigtriangleup$  ~ Allowed Reflow time: 2x max ~

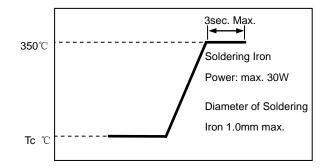
[Note: The reflow profile in the above table is only for qualification and is not meant to specify board assembly profiles. Actual board assembly profiles must be based on the customer's specific board design, solder paste and process, and should not exceed the parameters as the Reflow profile shows.]

#### 7.2 Iron Soldering Profile.

- $\triangle$  Iron soldering power: Max.30W
- $\bigtriangleup$   $\,$  Pre-heating: 150  $^\circ\!\!\mathrm{C}$  / 60sec.
- $\triangle$  Soldering Tip temperature: 350 °C Max.
- $\triangle$  Soldering time: 3sec Max.
- △ Solder paste: Sn/3.0Ag/0.5Cu
- $\triangle$  Max.1 times for iron soldering

[Note: Take care not to apply the tip of the soldering iron to the terminal electrodes.]

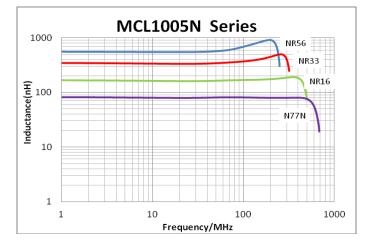


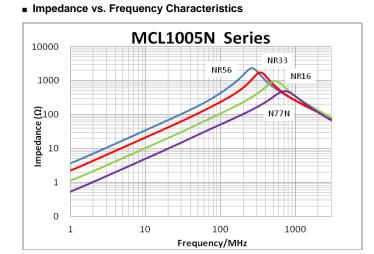


Appendix A: Electrical Characteristics

	L (nH)	L Test	L Test S.R.F Freq. Min. (MHz) (MHz)	DCR (Max) (Ω)	IDC(mA)		Thickness
Part Number					lsat (typ.) (ΔL/L=10%)	Irms(Max.) ΔT=25℃	(mm) [inch]
MCL1005N77N 🗆 T	77	25	200	0.27	550	550	
MCL1005N96N□T	96	25	200	0.35	500	500	
MCL1005NR10□T	100	25	200	0.35	500	500	
MCL1005NR11 DT	110	25	200	0.39	450	450	
MCL1005NR12□T	120	25	200	0.39	450	450	
MCL1005NR13DT	130	25	200	0.39	450	450	
MCL1005NR14□T	140	25	200	0.45	450	450	
MCL1005NR15□T	150	25	200	0.45	450	450	
MCL1005NR16□T	160	25	200	0.52	550	400	0.5±0.15 [0.020±0.006]
MCL1005NR18□T	180	25	200	0.58	370	400	[0.02010.000]
MCL1005NR20 T	200	25	200	0.58	370	400	
MCL1005NR22□T	220	25	180	0.58	370	400	
MCL1005NR27□T	270	25	180	0.65	350	350	
MCL1005NR33□T	330	25	120	0.65	300	350	
MCL1005NR39□T	390 25 120	120	0.97	300	300		
MCL1005NR47□T	470	25	120	0.97	250	300	
MCL1005NR56□T	560	25	120	1.4	250	250	

%Isat: DC current at which the inductance drops approximate 10% from its value without current; %Irms : DC current that causes the temperature rise ( $\triangle$ T =25°C) from 20°C ambient. % $\square$ : Please specify the inductance tolerance code (J=±5%,K=±10%,M=±20%). Inductance vs. Frequency Characteristics





Inductance vs. DC Current Characteristics

