

SPECIFICATIONS

Customer	
Product Name	Multi-layer Chip Power Inductor
Sunlord Part Number	MCL1608N Series
Customer Part Number	

New Released, Revised

SPEC No.: MCL0103210000

Rev.	Effective Date	Changed Contents	Change Reasons	Approved By
01	/	New release	/	Hai Guo

【This SPEC is total 11 pages.】

【ROHS, Halogen-Free and SVHC Compliant Parts】

Approved By	Checked By	Issued By

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【For Customer approval Only】

Date: _____

Qualification Status: Full Restricted Rejected

Approved By	Verified By	Re-checked By	Checked By

Comments:

Caution

All products listed in this specification are developed, designed and intended for use in general electronics equipment. The products are not designed or warranted to meet the requirements of the applications listed below, whose performance and/or quality require especially high reliability, or whose failure, malfunction or trouble might directly cause damage to society, person, or property. Please understand that we are not responsible for any damage or liability caused by use of the products in any of the applications below. Please contact us for more details if you intend to use our products in the following applications.

1. Aircraft equipment
2. Aerospace equipment
3. Undersea equipment
4. nuclear control equipment
5. military equipment
6. Power plant equipment
7. Medical equipment
8. Transportation equipment (automobiles, trains, ships,etc.
9. Traffic signal equipment
10. Disaster prevention / crime prevention equipment
11. Data-processing equipment
12. Applications of similar complexity or with reliability requirements comparable to the applications listed in the above

1. Scope

This specification applies to MCL1608N Series of multi-layer chip power inductors.

2. Product Description and Identification (Part Number)

- 1) Description
MCL1608N Series of multi-layer chip power inductors
- 2) Product Identification (Part Number)

MCL 1608 N R16 J T
 ① ② ③ ④ ⑤ ⑥

①Type	
MCL	Monolithic Type Power Inductor

②External Dimensions (L x W) (mm)	
1608	1.6×0.8

③Application Type	
N	Near Field Communication

④Nominal Inductance	
Example	Nominal Value
R16	0.16μH
R56	0.56μH

⑤Inductance Tolerance	
J	±5%
K	±10%
M	±20%

⑥Packing	
T	Tape Carrier Package

3. Electrical Characteristics

Please refer to **Appendix A** (Page 9).

- 1) Operating and storage temperature range (individual chip without packing): -40°C ~ +85°C
- 2) Storage temperature range (packaging conditions): -10°C~+40°C and RH 70% (Max.)

4. Shape and Dimensions

- 1) Dimensions and recommended PCB pattern for reflow soldering: See **Fig.4-1**, **Fig.4-2** and **Table 4-1**.
- 2) Structure: See **Fig. 4-3** and **Fig. 4-4**.

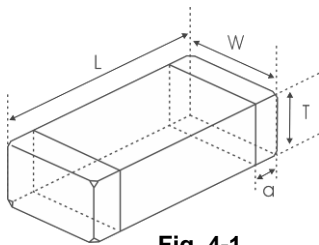


Fig. 4-1

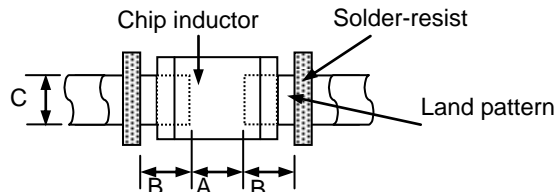


Fig. 4-2

[Table 4-1]

Unit: mm [inch]

Type	L	W	T	a	A	B	C
1608 [0603]	1.60±0.15 [0.063±0.006]	0.8±0.15 [0.031±0.006]	0.8±0.15 [0.031±0.006]	0.3±0.2 [0.012±0.008]	0.60~0.80	0.60~0.80	0.60~0.80

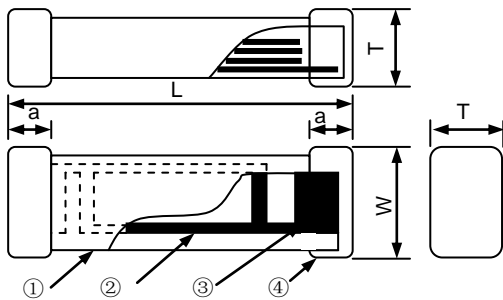


Fig. 4-3

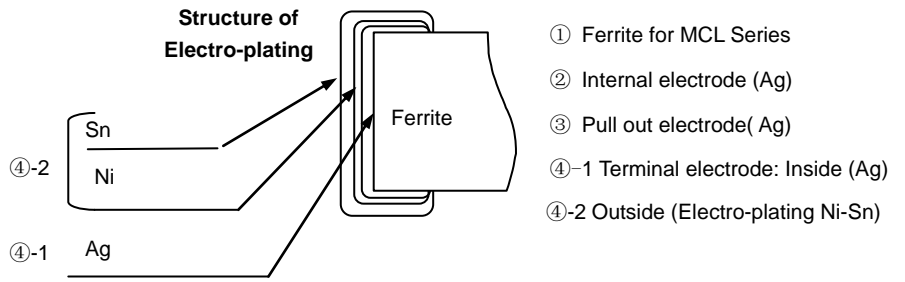


Fig. 4-4

- ① Ferrite for MCL Series
- ② Internal electrode (Ag)
- ③ Pull out electrode (Ag)
- ④-1 Terminal electrode: Inside (Ag)
- ④-2 Outside (Electro-plating Ni-Sn)

- 3) Material Information: See **Table 4-2**.

[Table 4-2]

Code	Part Name	Material Name
①	Ferrite Body	Ferrite Powder
②	Inner Coils	Silver Paste
③	Pull-out Electrode (Ag)	Silver Paste
④-1	Terminal Electrode: Inside Ag	Termination Silver Composition
④-2	Electro-Plating: Ni/Sn plating	Plating Chemicals

5. Test and Measurement Procedures

5.1 Test Conditions

Unless otherwise specified, the standard atmospheric conditions for measurement/test as:

- Ambient Temperature: $20 \pm 15^\circ\text{C}$
- Relative Humidity: $65 \pm 20\%$
- Air Pressure: 86kPa to 106kPa

If any doubt on the results, measurements/tests should be made within the following limits:

- Ambient Temperature: $20 \pm 2^\circ\text{C}$
- Relative Humidity: $65 \pm 5\%$
- Air Pressure: 86kPa to 106kPa

5.2 Visual Examination

- Inspection Equipment: 20x magnifier

5.3 Electrical Test

5.3.1 DC Resistance (DCR)

- Refer to **Appendix A**.
- Test equipment (Analyzer): High Accuracy Milliohmmeter-HP4338B or equivalent.

5.3.2 Inductance (L)

- Refer to **Appendix A**.
- Test equipment: High Accuracy RF Impedance /Material Analyzer-HP4291B+HP16192A or equivalent.
- Test signal: -20dBm or 50mV.
- Test frequency refers to **Appendix A**.

5.3.3 Temperature Rise Current (I_{rms})

- Refer to **Appendix A**.
- Test equipment (see **Fig. 5.3.3-1**): Electric Power, Electric current meter, Thermometer.
- Measurement method (see **Fig. 5.3.3-1**):
 - Set test current to be 0mA.
 - Measure initial temperature of chip surface.
 - Gradually increase voltage and measure chip temperature for corresponding current.
 - Definition of Temperature Rise Current (I_{rms}): I_{rms} is direct electric current as chip surface temperature rose just 25°C against chip initial surface temperature (T_a) (see **Fig. 5.3.3-2**)

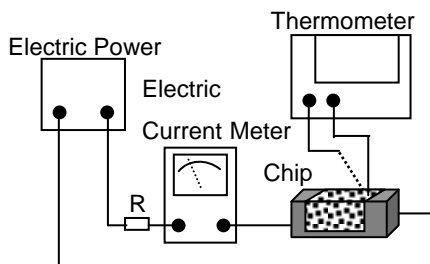


Fig. 5.3.3-1

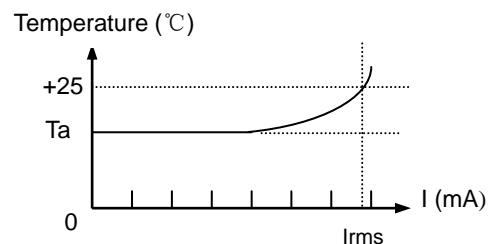


Fig. 5.3.3-2

5.3.4 Saturation Current (I_{sat})

- Refer to **Appendix A**.
- Test equipment: HP6632B system DC power supply, HP4291B+HP16192A+HP16200A or equivalent.
- Measurement method:
 - Measurement conditions of initial inductance L: Measuring Frequency: 25MHz.
Test Current: 1mA.
 - Definition of Saturation Current (I_{sat}): I_{sat} is the value of DC current as inductance L (nH) decreased just 10% against initial value (see **Fig. 5.3.4-1**).

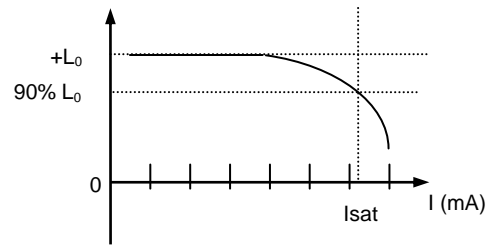
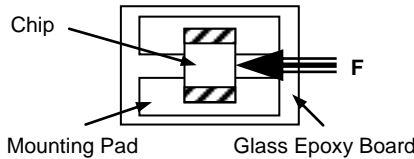
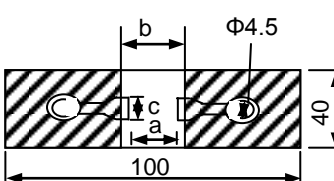
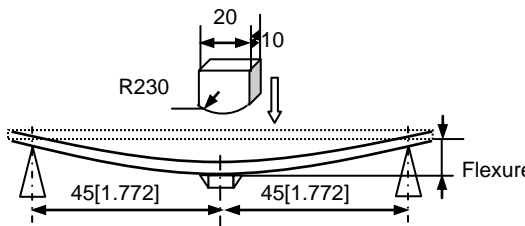
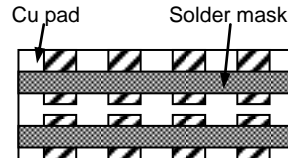


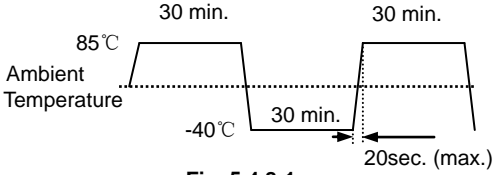
Fig. 5.3.4-1

5.3.5 Self-Resonant Frequency (SRF)

- a. Refer to **Appendix A**.
- b. Test equipment: High Accuracy RF Impedance /Material Analyzer-HP4291B+HP16192A or equivalent.
- c. Test signal: -20dBm or 50 mV.

5.4 Reliability Test

Items	Requirements	Test Methods and Remarks								
5.4.1 Terminal Strength	No removal or split of the termination or other defects shall occur.  <p style="text-align: center;">Fig.5.4.1-1</p>	<ol style="list-style-type: none"> ① Solder the inductor to the testing jig (glass epoxy board shown in Fig.5.4.1-1) using eutectic solder. Then apply a 10N force in the direction of the arrow. ② Keep time: 10±1s. ③ Speed: 1.0mm/s. 								
5.4.2 Resistance to Flexure	No visible mechanical damage. Unit: mm [inch] <table border="1" data-bbox="351 560 766 649" style="margin-left: auto; margin-right: auto;"> <thead> <tr> <th>Type</th> <th>a</th> <th>b</th> <th>c</th> </tr> </thead> <tbody> <tr> <td>1608[0603]</td> <td>1.0</td> <td>3.0</td> <td>1.2</td> </tr> </tbody> </table>  <p style="text-align: center;">Fig.5.4.2-1</p>	Type	a	b	c	1608[0603]	1.0	3.0	1.2	<ol style="list-style-type: none"> ① Solder the inductor to the test jig (glass epoxy board shown in Fig.5.4.2-1) Using a eutectic solder. Then apply a force in the direction shown Fig. 5.4.2-2. ② Flexure: 2mm. ③ Pressurizing Speed: 0.5mm/sec. ④ Keep time: 30 sec.  <p style="text-align: center;">Fig. 5.4.2-2</p>
Type	a	b	c							
1608[0603]	1.0	3.0	1.2							
5.4.3 Vibration	<ol style="list-style-type: none"> ① No visible mechanical damage. ② Inductance change: Within ±5%.  <p style="text-align: center;">Glass Epoxy Board Fig. 5.4.3-1</p>	<ol style="list-style-type: none"> ① Solder the inductor to the testing jig (glass epoxy board shown in Fig.5.4.3-1) using eutectic solder. ② The inductor shall be subjected to a simple harmonic motion having total amplitude of 1.5mm, the frequency being varied uniformly between the approximate limits of 10 and 55 Hz. ③ The frequency range from 10 to 55 Hz and return to 10 Hz shall be traversed in approximately 1 minute. This motion shall be applied for a period of 2 hours in each 3 mutually perpendicular directions (total of 6 hours). 								
5.4.4 Dropping	<ol style="list-style-type: none"> ① No visible mechanical damage. ② Inductance change: Within ±5%. 	Drop chip inductor 10 times on a concrete floor from a height of 100 cm.								
5.4.5 Temperature	Inductance change should be within ±20% of initial value measuring at 20℃.	Temperature range: -40℃~ +85℃ Reference temperature: +20℃								
5.4.6 Solderability	<ol style="list-style-type: none"> ① No visible mechanical damage. ② Wetting shall exceed 95% coverage. 	<ol style="list-style-type: none"> ① Solder temperature: 240±2℃ ② Duration: 3 sec. ③ Solder: Sn/3.0Ag/0.5Cu. ④ Flux: 25% Resin and 75% ethanol in weight. 								
5.4.7 Resistance to Soldering Heat	<ol style="list-style-type: none"> ① No visible mechanical damage. ② Wetting shall exceed 95% coverage. ③ Inductance change: Within ±5%. 	<ol style="list-style-type: none"> ① Solder temperature: 260±3℃. ② Duration: 5 sec. ③ Solder: Sn/3.0Ag/0.5Cu. ④ Flux: 25% Resin and 75% ethanol in weight. ⑤ The chip shall be stabilized at normal condition for 1~2 hours before measuring. 								

Items	Requirements	Test Methods and Remarks
5.4.8 Thermal Shock	① No mechanical damage. ② Inductance change: Within $\pm 5\%$.  <p style="text-align: center;">Fig. 5.4.8-1</p>	① Temperature, Time: (See Fig.5.4.8-1) -40°C for 30±3 min → 85°C for 30±3 min. ② Transforming interval: 20 sec.(max.). ③ Tested cycle: 100 cycles. ④ The chip shall be stabilized at normal condition for 1~2 hours before measuring.
5.4.9 Resistance to Low Temperature	① No mechanical damage. ② Inductance change: Within $\pm 5\%$.	① Temperature: $-40\pm 2^\circ\text{C}$ ② Duration: 1000 ⁺²⁴ hours. ③ The chip shall be stabilized at normal condition for 1~2 hours before measuring.
5.4.10 Resistance to High Temperature	① No mechanical damage. ② Inductance change: Within $\pm 5\%$.	① Temperature: $85\pm 2^\circ\text{C}$ ② Duration: 1000 ⁺²⁴ hours. ③ The chip shall be stabilized at normal condition for 1~2 hours before measuring.
5.4.11 Damp Heat (Steady States)	① No visible mechanical damage. ② Inductance change: Within $\pm 5\%$.	① Temperature: $60\pm 2^\circ\text{C}$ ② Humidity: 90% to 95% RH. ③ Duration: 1000 ⁺²⁴ hours. ④ The chip shall be stabilized at normal condition for 1~2 hours before measuring.
5.4.12 Loading Under Damp Heat	① No visible mechanical damage. ② Impedance change: within $\pm 5\%$.	① Temperature: $60\pm 2^\circ\text{C}$. ② Humidity: 90% to 95% RH. ③ Duration: 1000 ⁺²⁴ hours. ④ Applied current: Rated current. ⑤ The chip shall be stabilized at normal condition for 1~2 hours before measuring.
5.4.13 Loading at High Temperature (Life Test)	① No visible mechanical damage. ② Impedance change: within $\pm 5\%$.	① Temperature: $85\pm 2^\circ\text{C}$ ② Duration: 1000 ⁺²⁴ hours. ③ Applied current: Rated current. ④ The chip shall be stabilized at normal condition for 1~2 hours before measuring.

6. Packaging and Storage

6.1 Packaging

Tape Carrier Packaging:

Packaging code: T

- a. Tape carrier packaging are specified in attached figure **Fig.6.1-1~3**
- b. Tape carrier packaging quantity please see the following table:

Type	1608[0603]
T(mm)	0.8±0.15
Tape	Paper Tape
Quantity	4K

- c. Reel shall be packaged in vinyl bag.
- d. Maximum of 5 or 10 reels bags shall be packaged in an inner box.
- e. Maximum of 6 or 10 inner boxes shall be packaged in an outer case.

(1) Taping Drawings (Unit: mm)

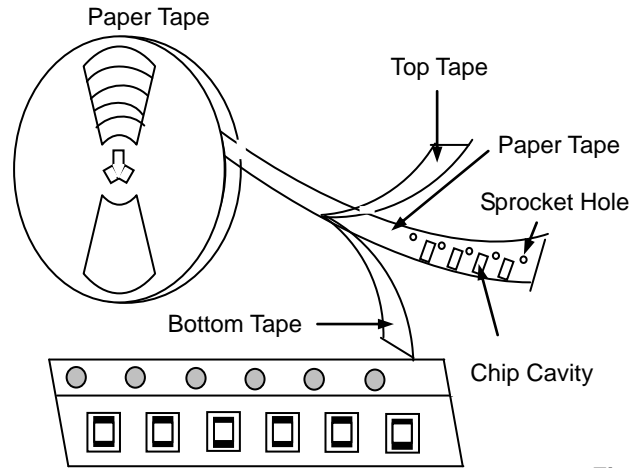
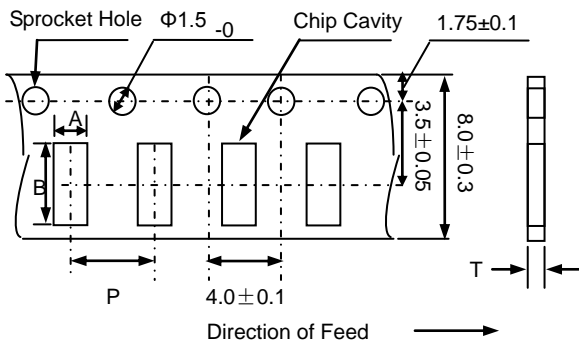


Fig. 6.1-1

Remark: The sprocket holes are to the right as the tape is pulled toward the user.

(2) Taping Dimensions (Unit: mm)



Type	A	B	P	T max
1608[0603]	1.0±0.2	1.8±0.2	4.0±0.1	1.1

Fig. 6.1-2

(3) Reel Dimensions (Unit: mm)

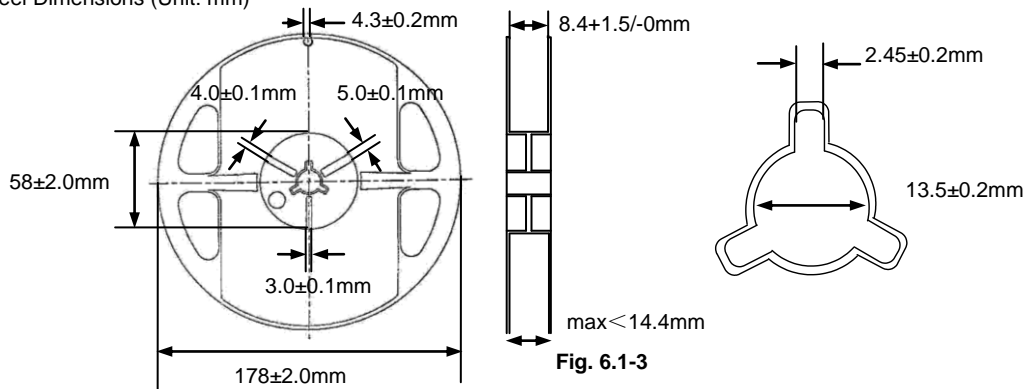


Fig. 6.1-3

6.2 Storage

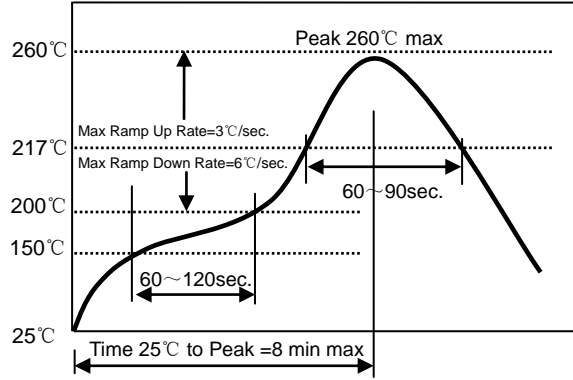
- The solderability of the external electrode may be deteriorated if packages are stored where they are exposed to high humidity. Package must be stored at 40°C or less and 70% RH or less.
- The solderability of the external electrode may be deteriorated if packages are stored where they are exposed to dust or harmful gas (e.g. HCl, sulfurous gas of H₂S).
- Packaging material may be deformed if package are stored where they are exposed to heat or direct sunlight.
- Solderability specified in **Clause 5.4.6** shall be guaranteed for 12 months from the date of delivery on condition that they are stored at the environment specified in **Clause 3**. For those parts, which passed more than 12 months shall be checked solder-ability before use.

7. Recommended Soldering Technologies

7.1 Reflowing Profile:

- △ Preheat condition: 150 ~200°C/60~120sec.
- △ Allowed time above 217°C: 60~90sec.
- △ Max temp: 260°C
- △ Max time at max temp: 10sec.
- △ Solder paste: Sn/3.0Ag/0.5Cu
- △ Allowed Reflow time: 2x max

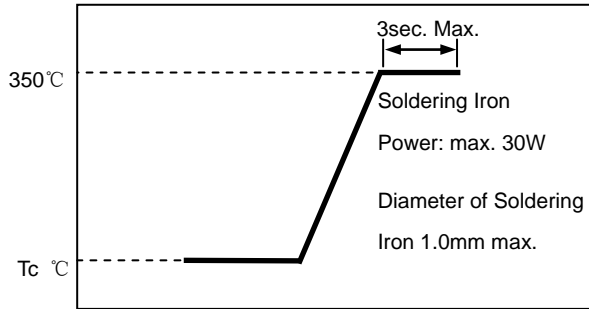
[Note: The reflow profile in the above table is only for qualification and is not meant to specify board assembly profiles. Actual board assembly profiles must be based on the customer's specific board design, solder paste and process, and should not exceed the parameters as the Reflow profile shows.]



7.2 Iron Soldering Profile.

- △ Iron soldering power: Max.30W
- △ Pre-heating: 150 °C / 60sec.
- △ Soldering Tip temperature: 350°C Max.
- △ Soldering time: 3sec Max.
- △ Solder paste: Sn/3.0Ag/0.5Cu
- △ Max.1 times for iron soldering

[Note: Take care not to apply the tip of the soldering iron to the terminal electrodes.]



Appendix A: Electrical Characteristics

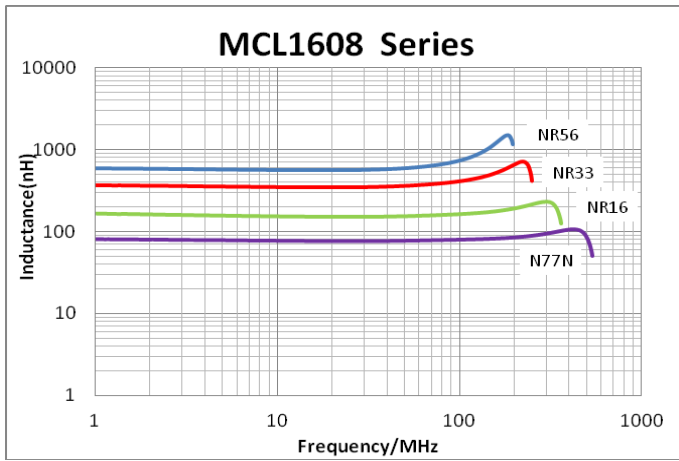
Part Number	L (nH)	L Test Freq. (MHz)	S.R.F Min. (MHz)	DCR (Max) (Ω)	IDC(mA)		Thickness (mm) [inch]
					Isat (typ.) ($\Delta L/L=10\%$)	Irms(Max.) $\Delta T=25^{\circ}\text{C}$	
MCL1608N77N□T	77	25	200	0.11	1100	1100	0.8±0.15 [0.031±0.006]
MCL1608N85N□T	85	25	200	0.11	1100	1100	
MCL1608NR10□T	100	25	200	0.12	1000	1000	
MCL1608NR12□T	120	25	200	0.14	1000	800	
MCL1608NR16□T	160	25	200	0.156	1100	700	
MCL1608NR20□T	200	25	200	0.22	700	650	
MCL1608NR21□T	210	25	200	0.26	700	600	
MCL1608NR22□T	220	25	200	0.26	700	600	
MCL1608NR27□T	270	25	200	0.286	650	550	
MCL1608NR33□T	330	25	180	0.312	650	500	
MCL1608NR39□T	390	25	180	0.36	600	450	
MCL1608NR47□T	470	25	120	0.494	600	400	
MCL1608NR56□T	560	25	120	0.52	550	400	
MCL1608NR65□T	650	25	100	0.65	450	350	
MCL1608NR82□T	820	25	80	0.75	400	300	

※Isat: DC current at which the inductance drops approximate 10% from its value without current;

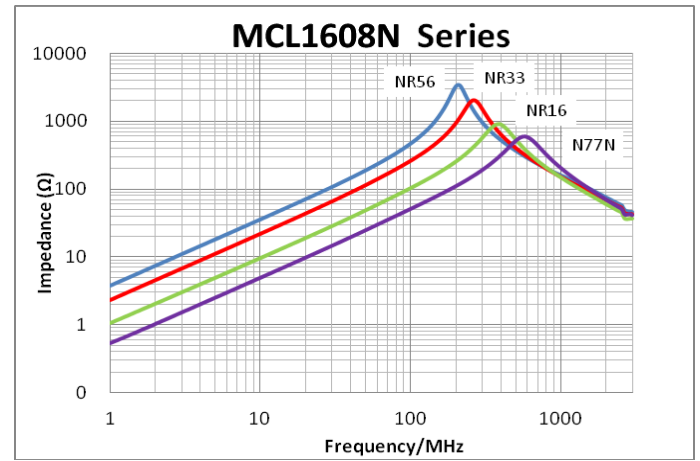
※Irms : DC current that causes the temperature rise ($\Delta T = 25^{\circ}\text{C}$) from 20°C ambient.

※□: Please specify the inductance tolerance code (J=±5%,K=±10%,M=±20%).

Inductance vs. Frequency Characteristics



■ Impedance vs. Frequency Characteristics



Inductance vs. DC Current Characteristics

