SPECIFICATIONS

Customer								
Product Name		Multi-layer Chip Inductor for Choke						
Sunlord Part N	umber		MCL2012 Series					
				IVIC	LZUIZ S	ei 162		
Customer Part	Number							
oxtimeNew Release	d, ⊡Revi	ised]			SPEC	No.: M	CL0706210	000
This SPEC is to	otal 9 pages	includi	ing speci	fications	and appen	dix.]		
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	Approve	d By	Check	ed By	Issued	d By		
Shenzhe Idress: Sunlord Indu I: 0086-755-298323	ustrial Park,	Dafu Ir		Zone, Bac	an, Shenz	hen, Chi)
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Qualification Statu	· -	Full	□ Re	estricted		jected		
Approved By	<i>'</i>	/erified	Ву	Re-ch	ecked By	(Checked By	
Comments:								

Sunlord Business categories: Level 0 (general confidential) Specifications for Multi-layer Chip Inductor for Choke Page 2 of 9

【Version change history】

Rev.	Effective Date	Changed Contents	Change reasons	Approved By
01	/	New release	1	Hai Guo

Caution

All products listed in this specification are developed, designed and intended for use in general electronics equipment. The products are not designed or warranted to meet the requirements of the applications listed below, whose performance and/or quality require especially high reliability, or whose failure, malfunction or trouble might directly cause damage to society, person, or property. Please understand that we are not responsible for any damage or liability caused by use of the products in any of the applications below. Please contact us for more details if you intend to use our products in the following applications.

- 1. Aircraft equipment
- 2. Aerospace equipment
- 3. Undersea equipment
- 4. nuclear control equipment
- 5. military equipment
- 6. Power plant equipment
- 7. Medical equipment
- 8. Transportation equipment (automobiles, trains, ships,etc.)
- 9. Traffic signal equipment
- 10. Disaster prevention / crime prevention equipment
- 11. Data-processing equipment
- 12. Applications of similar complexity or with reliability requirements comparable to the applications listed in the above

1. Scope

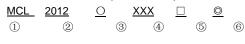
This specification applies to MCL2012 Series of multi-layer chip inductors for choke.

2. Product Description and Identification (Part Number)

1) Description

MCL2012 Series of multi-layer chip inductors for choke.

2) Product Identification (Part Number)



1	Туре
MCL	Chip Inductor for choke

2	External Dimensions (L x W) (mm)		
	2012 [0805]	2.0 X 1.25	

③ Feature Type			
S Standard			
H IDC-Improved			

⑤ Inductance Tolerance				
М	±20%			
N	±30%			

④ Nomina	Nominal Inductance			
Example	Nominal Value			
1R0	1.0µH			
100	10µH			

6	6 Packing		
	Т		Tape Carrier Package

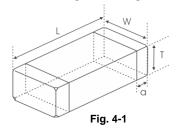
3. Electrical Characteristics

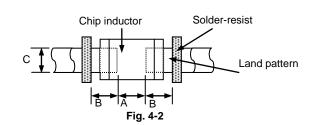
Please refer to Appendix A (Page 9).

- 1) Operating and storage temperature range (individual chip without packing): -55 $^{\circ}$ ~ +125 $^{\circ}$ (Including Self-heating)
- 2) Storage temperature range (packaging conditions): -10°C~+40°C and RH 70% (Max.)

4. Shape and Dimensions

- 1) Dimensions and recommended PCB pattern for reflow soldering: See Fig.4-1, Fig.4-2 and Table 4-1.
- 2) Structure: See Fig. 4-3 and Fig. 4-4.

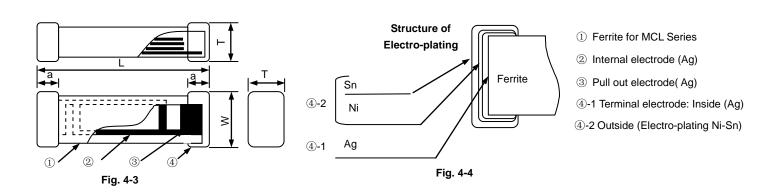




[Table 4-1]

Unit: mm [inch]

Туре	L	W	Т	а	А	В	С
2012	2.0 (+0.3, -0.1)	1.25±0.2	0.85±0.2[0.033±0.008]	0.5±0.3	0.00 4.00	0.00 4.00	0.00 4.00
[0805]	[0.079 (+0.012, -0.004)]	[0.049±0.008]	1.25±0.2[0.049±0.008]	[0.020±0.012]	0.80~1.20	0.80~1.20	0.90~1.60



3) Material Information: See Table 4-2.

Г	Гab	le	4-2

Code	Part Name	Material Name	
1	Ferrite Body	Ferrite Powder	
2	Inner Coils	Silver Paste	
3	Pull-out Electrode (Ag)	Silver Paste	
4 -1	Terminal Electrode: Inside Ag	Termination Silver Composition	
4 -2	Electro-Plating: Ni/Sn plating	Plating Chemicals	

5. Test and Measurement Procedures

5.1 Test Conditions

5.1.1Unless otherwise specified, the standard atmospheric conditions for measurement/test as:

a. Ambient Temperature: 20±15°C
b. Relative Humidity: 65±20%
c. Air Pressure: 86kPa to 106kPa

5.1.2If any doubt on the results, measurements/tests should be made within the following limits:

a. Ambient Temperature: 20±2°Cb. Relative Humidity: 65±5%

c. Air Pressure: 86kPa to 106kPa

5.2 Visual Examination

a. Inspection Equipment: 20x magnifier

5.3 Electrical Test

- 5.3.1 DC Resistance (DCR)
 - a. Refer to Appendix A.
 - b. Test equipment (Analyzer): High Accuracy Milliohmmeter-HP4338B or equivalent.

5.3.2 Inductance (L)

- a. Refer to Appendix A.
- b. Test equipment: High Accuracy RF Impedance /Material Analyzer-E4991A +HP16192A or equivalent.
- c. Test signal: -20dBm or 50mV.
- d. Test frequency refers to Appendix A.
- 5.3.3 Self-Resonant Frequency (SRF)
 - a. Refer to Appendix A.
 - b. Test equipment: High Accuracy RF Impedance/Material Analyzer-E4991A +HP16192A or equivalent.
 - c. Test signal: -20dBm or 50 mV.

5.3.4 Rated Current

- a. Refer to Appendix A.
- b. Test equipment: HP6632B system DC power supply, -E4991A +HP16192A+HP16200A or equivalent.
- c. Measurement method:
 - 1. Measurement conditions of initial inductance L: Measuring Frequency: 1MHz.

Test Current: $1.0\mu H\sim 4.7\mu H$, 1mA; $10\mu H$, 0.1mA.

2. Raising the voltage of the DC power supply, measure the inductance at the various current.

The rated current is the value of DC current at which the inductance will be 50% down compared with the initial inductance value.

Note: In the period of raising voltage, voltage cannot be reduced.

d. Definition of Rated Current (Ir): Ir is the value of DC current as inductance L (μH) decreased just 50% against initial value (see **Fig. 5.3.4-1**).

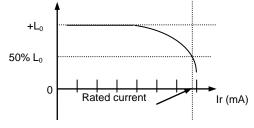


Fig. 5.3.4-1

5.4 Reliability Test

Items	Requirements	Test Methods and Remarks
5.4.1 Terminal Strength	No removal or split of the termination or other defects shall occur.	 Solder the inductor to the testing jig (glass epoxy board shown in Fig.5.4.1-1) using leadfree solder. Then apply a 10N force in the direction of the arrow. Keep time: 10±1s. Speed: 1.0mm/s.
	Mounting Pad Glass Epoxy Board Fig.5.4.1-1	
5.4.2 Resistance to Flexure	No visible mechanical damage. Type a b c 2012[0805] 1.2 4.0 1.65	 Solder the inductor to the test jig (glass epoxy board shown in Fig.5.4.2-1) Using a leadfree solder. Then apply a force in the direction shown Fig. 5.4.2-2. Flexure: 2mm. Pressurizing Speed: 0.5mm/sec. Keep time: 30 sec.
	Unit: mm [inch]	Fig.5.4.2-2
5.4.3 Vibration	No visible mechanical damage. Cu pad Solder mask Glass Epoxy Board Fig. 5.4.3-1	 Solder the inductor to the testing jig (glass epoxy board shown in Fig.5.4.3-1) using leadfree solder. The inductor shall be subjected to a simple harmonic motion having total amplitude of 1.5mm, the frequency being varied uniformly between the approximate limits of 10 and 55 Hz. The frequency range from 10 to 55 Hz and return to 10 Hz shall be traversed in approximately 1 minute. This motion shall be applied for a period of 2 hours in each 3mutually perpendicular directions (total of 6 hours).
5.4.4	Inductance change should be within ±20% of	Temperature range: -40°C~ +85°C
Temperature	initial value measuring at 20℃.	Reference temperature: +20 ℃
5.4.5 Solderability	 No visible mechanical damage. Wetting shall exceed 95% coverage. 	 Solder temperature: 240±2°C Duration: 3sec. Solder: Sn/3.0Ag/0.5Cu. Flux: 25% Resin and 75% ethanol in weight.
5.4.6 Resistance to Soldering Heat	 No visible mechanical damage. Wetting shall exceed 95% coverage. Inductance change: Within ±20%. 	 Solder temperature: 260±3℃. Duration: 5sec. Solder: Sn/3.0Ag/0.5Cu. Flux: 25% Resin and 75% ethanol in weight. The chip shall be stabilized at normal condition for 1~2 hours before measuring.
5.4.7 Thermal Shock	1 No mechanical damage. 2 Inductance change: Within ±20%. 85°C 30 min. 30 min. 40°C Fig. 5.4.7-1 20sec. (max.)	 Temperature, Time: (See Fig.5.4.7-1) -40 ℃ for 30±3 min→ 85 ℃ for 30±3min. Transforming interval: 20sec.(max.). Tested cycle: 100 cycles. The chip shall be stabilized at normal condition for 1~2 hours before measuring.

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5.4.8	No mechanical damage.	① Temperature: -40±2°C
Resistance to Low Temperature	② Inductance change: Within ±20%.	 Duration: 1000⁺²⁴ hours. The chip shall be stabilized at normal condition for 1~2 hours before measuring.
5.4.9 Loading Under Damp Heat	No visible mechanical damage. Inductance change: within ±20%.	 Temperature: 60±2°C Humidity: 90% to 95% RH. Duration: 1000⁺²⁴ hours. Applied current: Rated current. The chip shall be stabilized at normal condition for 1~2 hours before measuring.
5.4.10 Loading at High Temperature (Life Test)	No visible mechanical damage. Inductance change: within ±20%.	 Temperature: 85±2°C Duration: 1000⁺²⁴ hours. Applied current: Rated current. The chip shall be stabilized at normal condition for 1~2 hours before measuring.

6. Packaging, Storage

6.1 Packaging

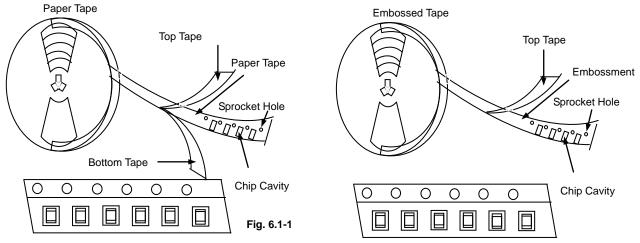
Tape Carrier Packaging:

Packaging code: T

- a. Tape carrier packaging are specified in attached figure Fig.6.1-1~4
- b. Tape carrier packaging quantity please see the following table:

Туре	2012[0805]			
T(mm)	0.85±0.2	1.25±0.2		
Tape	Paper Tape	Embossed Tape		
Quantity	4K	3K		

(1) Taping Drawings (Unit: mm)



Remark: The sprocket holes are to the right as the tape is pulled toward the user.

(2) Taping Dimensions (Unit: mm)

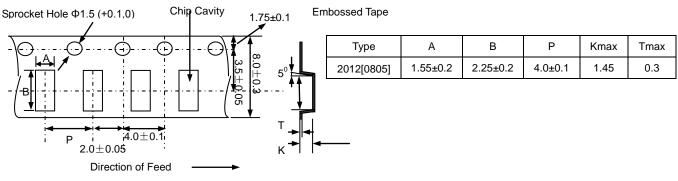
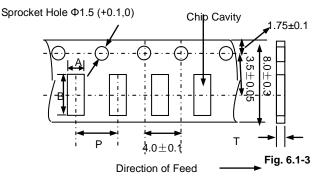


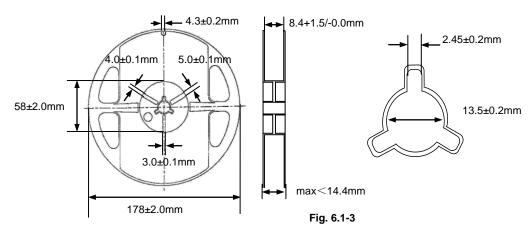
Fig. 6.1-2



Paper Tape

Туре	А	В	Р	T max
2012[0805]	1.5±0.2	2.3±0.2	4.0±0.1	1.1

(3) Reel Dimensions (Unit: mm)



6.2 Storage

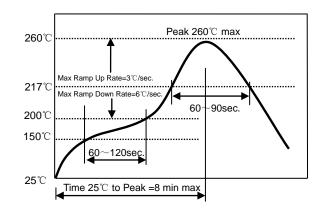
- a. The solderability of the external electrode may be deteriorated if packages are stored where they are exposed to high humidity. Package must be stored at 40° C or less and 70% RH or less.
- b. The solderability of the external electrode may be deteriorated if packages are stored where they are exposed to dust of harmful gas (e.g. HCl, sulfurous gas of H_2S).
- c. Packaging material may be deformed if package are stored where they are exposed to heat of direct sunlight.
- d. Solderability specified in **Clause 5.4.6** shall be guaranteed for 12months from the date of delivery on condition that they are stored at the environment specified in **Clause 3**. For those parts, which passed more than 12 months shall be checked solder-ability before use.

7. Recommended Soldering Technologies

7.1 Re-flowing Profile:

- \triangle Preheat condition: 150 ~200 °C/60~120sec.
- △ Allowed time above 217°C: 60~90sec.
- △ Max temp: 260°C
- \triangle Max time at max temp: 10sec.
- △ Solder paste: Sn/3.0Ag/0.5Cu
- \triangle Allowed Reflow time: 2x max

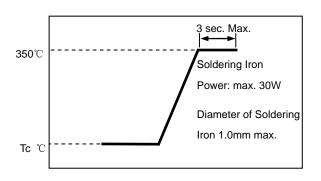
[Note: The reflow profile in the above table is only for qualification and is not meant to specify board assembly profiles. Actual board assembly profiles must be based on the customer's specific board design, solder paste and process, and should not exceed the parameters as the Reflow profile shows.]



7.2 Iron Soldering Profile.

- △ Iron soldering power: Max.30W.
- \triangle Pre-heating: 150 $^{\circ}$ C / 60 sec.
- △ Soldering Tip temperature: 350 °C Max.
- △ Soldering time: 3sec Max.
- △ Solder paste: Sn/3.0Ag/0.5Cu.
- \triangle Max.1 times for iron soldering.

[Note: Take care not to apply the tip of the soldering iron to the terminal electrodes.]



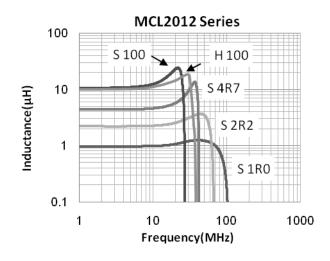
Appendix A: Electrical Characteristics

Part Number	L	L Test Freq.	S.R.F	DCR	(Ω)	lr	Thickness
	(µH)	(MHz)	Min. (MHz)	(Тур.)	(Max.)	Max. (mA)	(mm) [inch]
MCL2012SR10□T	0.1	1	235	0.070	0.091	1000	
MCL2012SR22□T	0.22	1	170	0.130	0.169	800	
MCL2012SR47□T	0.47	1	125	0.180	0.234	550	0.85±0.2
MCL2012S1R0□T	1.0	1	75	0.200	0.260	300	[.033±.008]
MCL2012S2R2□T	2.2	1	50	0.280	0.364	220	
MCL2012S4R7□T	4.7	1	25	0.300	0.390	180	
MCL2012S100□T	10	1	15	0.500	0.650	60	1.25±0.2
MCL2012H100□T	10	1	20	0.500	0.650	100	[.049±.008]

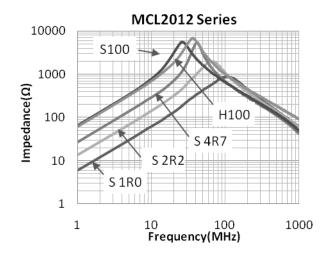
 $[\]times \square$: Please specify the inductance tolerance code (M=±20%, N=±30%).

Typical Electrical Characteristics

Inductance vs. Frequency Characteristics



Impedance vs. Frequency Characteristics



Inductance vs. DC Current Characteristics

