SPECIFICATIONS

Customer	
Product Name	Multi-layer Chip Power Inductor
Sunlord Part Number	MPH201612S Series
Customer Part Number	
New Released, Revi	ised] SPEC No.: MPH0209210000

[This SPEC is total 10 pages including specifications and appendix.] [ROHS, Halogen-Free and SVHC Compliant Parts]

Approved By	Checked By	Issued By

Shenzhen Sunlord Electronics Co., Ltd.

Address: Sunlord Industrial Park, Dafu Industrial Zone, Baoan, Shenzhen, China518110Tel: 0086-755-29832333Fax: 0086-755-82269029E-Mail: sunlord@sunlordinc.com

[For Customer approval Only]			4	Date		4	
Qualification Status:			estricted		Rejec	ted	
Approved By	Verified B	y	Re-ch	ecked	By	Checked By	
Comments:							

【Version change history】

Rev.	Effective Date	Changed Contents	Change Reasons	Approved By
01	/	New release	1	Hai Guo

Caution

All products listed in this specification are developed, designed and intended for use in general electronics equipment. The products are not designed or warranted to meet the requirements of the applications listed below, whose performance and/or quality require especially high reliability, or whose failure, malfunction or trouble might directly cause damage to society, person, or property. Please understand that we are not responsible for any damage or liability caused by use of the products in any of the applications below. Please contact us for more details if you intend to use our products in the following applications.

- 1. Aircraft equipment
- 2. Aerospace equipment
- 3. Undersea equipment
- 4. nuclear control equipment
- 5. military equipment
- 6. Power plant equipment
- 7. Medical equipment
- 8. Transportation equipment (automobiles, trains, ships, etc.)
- 9. Traffic signal equipment
- 10. Disaster prevention / crime prevention equipment
- 11. Data-processing equipment
- 12. Applications of similar complexity or with reliability requirements comparable to the applications listed in the above

1. Scope

This specification applies to MPH201612S Series of multi-layer chip power inductors

Product Description and Identification (Part Number) 2.

- Description 1)
 - MPH201612 Series of multi-layer chip power inductors.

2)	Produc	t Identificati	on (Par	t Number)		
	<u>MPH</u>	<u>201612</u>	<u>S</u>	XXX		T
	1	2	3	4	5	\bigcirc

1)Type MPH Monolithic Type Power Inductor

③Feature Type	
S	Standard

⑤Inductance Tolerance		
М	±20%	
N	±30%	

②External Dimensions (L x W xH) (mm)		
201612	2.0×1.6×1.2	

(4) Nominal Inductance	
Example	Nominal Value
6R8	6.8µH
100	10 µH

⑦Packing	
Т	Tape Carrier Package

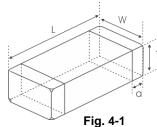
Electrical Characteristics 3.

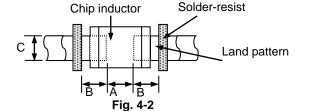
Please refer to Appendix A (Page 8).

- Operating and storage temperature range (individual chip without packing): -55°C ~ +125°C (Including Self-heating) 1)
- 2) Storage temperature range (packaging conditions): -10°C ~+40°C and RH 70% (Max.)

4. Shape and Dimensions

- 1) Dimensions and recommended PCB pattern for reflow soldering: See Fig.4-1, Fig.4-2 and Table 4-1.
- 2) Structure: See Fig. 4-3 and Fig. 4-4.

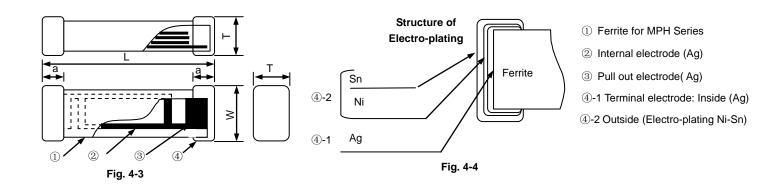






Unit: mm [inch]

Туре	L	W	Т	а	А	В	С
201612	2.0 (+0.3, -0.1) [.079 (+.012,004)]	1.6±0.2 [.063±.008]	1.1±0.1 [.043±.004]	0.5±0.3 [.020±.012]	0.8~1.2	0.8~1.2	1.2~2.0



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3) Material Information: See Table 4-2.

[Table 4-2]

Code	Part Name	Material Name
1	Ferrite Body	Ferrite Powder
2	Inner Coils	Silver Paste
3	Pull-out Electrode (Ag)	Silver Paste
④-1	Terminal Electrode: Inside Ag	Termination Silver Composition
④-2	Electro-Plating: Ni/Sn plating	Plating Chemicals

5. Test and Measurement Procedures

5.1 Test Conditions

Unless otherwise specified, the standard atmospheric conditions for measurement/test as:

- a. Ambient Temperature: 20±15°C
- b. Relative Humidity: 65±20%
- c. Air Pressure: 86kPa to 106kPa
- If any doubt on the results, measurements/tests should be made within the following limits:
 - a. Ambient Temperature: 20±2°C
 - b. Relative Humidity: 65±5%
 - c. Air Pressure: 86kPa to 106kPa

5.2 Visual Examination

a. Inspection Equipment: 20x magnifier

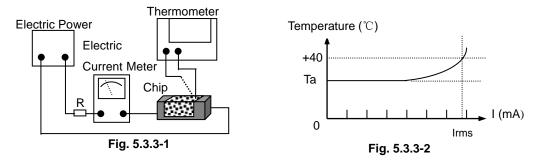
5.3 Electrical Test

- 5.3.1 DC Resistance (DCR)
 - a. Refer to Item3.
 - b. Test equipment (Analyzer): High Accuracy Milliohmmeter-HP4338B or equivalent.

5.3.2 Inductance (L)

- a. Refer to Item3.
- b. Test equipment: High Accuracy RF Impedance /Material Analyzer-HP4291B+HP16192A or equivalent.
- c. Test signal: -20dBm or 50mV.
- d. Test frequency refers to Item3.
- 5.3.3 Temperature Rise Current (Irms)
 - a. Refer to Item3.
 - b. Test equipment (see Fig. 5.3.3-1): Electric Power, Electric current meter, Thermometer.
 - c. Measurement method (see Fig. 5.3.3-1):
 - 1. Set test current to be 0 mA.
 - 2. Measure initial temperature of chip surface.
 - 3. Gradually increase voltage and measure chip temperature for corresponding current.

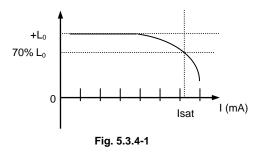
4. Definition of Temperature Rise Current (Irms) : I_{DC}1 is direct electric current as chip surface temperature rose just 40°C against chip initial surface temperature (Ta) (see **Fig. 5.3.3-2**)



5.3.4 Saturation Current (Isat)

- a. Refer to Item3.
- b. Test equipment: HP6632B system DC power supply, HP4291B+HP16192A+HP16200A or equivalent.
- c. Measurement method:
 - 1. Measurement conditions of initial inductance L: Measuring Frequency: 1MHz.
 - Test Current: 1mA.

2. Definition of Saturation Current (Isat) : $I_{DC}2$ is the value of DC current as inductance L (µH) decreased just 30% against initial value (see Fig. 5.3.4-1).



5.3.5Self-Resonant Frequency (SRF)

- a. Refer to Item3.
- b. Test equipment: High Accuracy RF Impedance /Material Analyzer-HP4291B+HP16192A or equivalent.
- c. Test signal: -20dBm or 50 mV.

5.4 Reliability Test

Items	Requirements	Test Methods and Remarks			
5.4.1 Terminal	No removal or split of the termination or other defects shall occur.	 Solder the inductor to the testing jig (glass epoxy board shown in Fig.5.4.1-1) using eutectic solder. Then apply a 			
Strength	Chip Chip F Mounting Pad Glass Epoxy Board Fig.5.4.1-1	 10N force in the direction of the arrow. (2) Keep time: 10±1s. (3) Speed: 1.0mm/s. 			
5.4.2 Resistance	No visible mechanical damage.	 Solder the inductor to the test jig (glass epoxy board shown in Fig.5.4.2-1) Using a eutectic solder. Then apply a force in 			
to Flexure	Unit: mm [inch]	the direction shown Fig. 5.4.2-2.			
	Type a b c	 Plexure: 2mm. Pressurizing Speed: 0.5mm/sec. 			
	201612 0.8 2.4 1.4	 ④ Keep time: 30 sec. 			
	b ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓	R230 45[1.772] Fig. 5.4.2-2			

Items	Requirements	Test Methods and Remarks 1 Solder the inductor to the testing jig (glass epoxy board shown in Fig.5.4.3-1) using eutectic solder. 2 The inductor shall be subjected to a simple harmonic motion having total amplitude of 1.5mm, the frequency being varied uniformly between the approximate limits of 10 and 55 Hz. 3 The frequency range from 10 to 55 Hz and return to 10 Hz shall be traversed in approximately 1 minute. This motion shall be applied for a period of 2 hours in each 3mutually perpendicular directions (total of 6 hours).			
5.4.3 Vibration	 No visible mechanical damage. Inductance change: Within ±20%. Cu pad Solder mask Glass Epoxy Board Fig. 5.4.3-1 				
5.4.4 Dropping 5.4.5 Temperature	 No visible mechanical damage. Inductance change: Within ±20%. Inductance change should be within ±20% of initial value measuring at 20°C. 	Drop chip inductor 10 times on a concrete floor from a height of 100 cm. Temperature range: -40°C~ +85°C Reference temperature: +20°C			
5.4.6 Solderability	 No visible mechanical damage. Wetting shall exceed 95% coverage. 	 Solder temperature: 240±2℃ Duration: 3 sec. Solder: Sn/3.0Ag/0.5Cu. Flux: 25% Resin and 75% ethanol in weight. 			
5.4.7 Resistance to Soldering Heat	 No visible mechanical damage. Wetting shall exceed 95% coverage. Inductance change: Within ±20%. 	 Solder temperature: 260±3°C. Duration: 5 sec. Solder: Sn/3.0Ag/0.5Cu. Flux: 25% Resin and 75% ethanol in weight. The chip shall be stabilized at normal condition for 1~2 hours before measuring. 			
5.4.8 Thermal Shock	 No mechanical damage. Inductance change: Within ±20%. 30 min. 	 Temperature, Time: (See Fig.5.4.8-1) -40°C for 30±3 min→ 85°C for 30±3min. Transforming interval: 20 sec.(max.). Tested cycle: 100 cycles. The chip shall be stabilized at normal condition for 1~2 hours before measuring. 			
5.4.9 Resistance to Low Temperature	 No mechanical damage. Inductance change: Within ±20%. 	 Temperature: -40±2°C Duration: 1000⁺²⁴ hours. The chip shall be stabilized at normal condition for 1~2 hours before measuring. 			
5.4.10 Resistance to High Temperature	 No mechanical damage. Inductance change: Within ±20%. 	 Temperature: 85±2°C Duration: 1000⁺²⁴ hours. The chip shall be stabilized at normal condition for 1~2 hours before measuring. 			
5.4.11 Damp Heat (Steady States)	 No visible mechanical damage. Inductance change: Within ±20%. 	 Temperature: 60±2℃ Humidity: 90% to 95% RH. Duration: 1000⁺²⁴ hours. The chip shall be stabilized at normal condition for 1~2 hours before measuring. 			

6. Packaging and Storage

6.1 Packaging

Tape Carrier Packaging:

- Packaging code: T
- a. Tape carrier packaging are specified in attached figure Fig.6.1-1~4
- b. Tape carrier packaging quantity please see the following table:

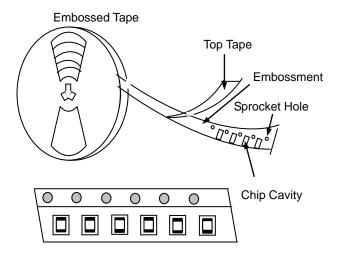
Туре	201612			
T(mm)	1.1±0.1			
Таре	Embossed Tape			
Quantity	ЗК			

- c. Reel shall be packaged in vinyl bag.
- d. Maximum of 5 or 10 reels bags shall be packaged in an inner box.

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e. Maximum of 6 or 10 inner boxes shall be packaged in an outer case.

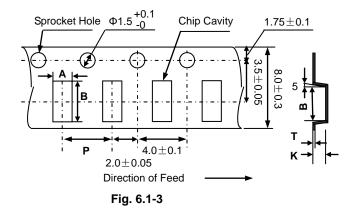
(1) Taping Drawings (Unit: mm)





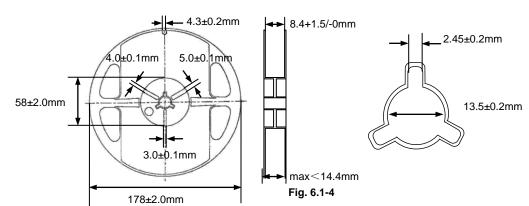
Remark: The sprocket holes are to the right as the tape is pulled toward the user.

(2) Taping Dimensions (Unit: mm)



Туре	A B		Р	Kmax	Tmax
MPH201612	1.90±0.1	2.30±0.1	4.0±0.1	1.45	0.3

(3) Reel Dimensions (Unit: mm)



6.2 Storage

- a. The solderability of the external electrode may be deteriorated if packages are stored where they are exposed to high humidity. Package must be stored at 40°C or less and 70% RH or less.
- b. The solderability of the external electrode may be deteriorated if packages are stored where they are exposed to dust of harmful gas (e.g. HCl, sulfurous gas of H₂S).
- c. Packaging material may be deformed if package are stored where they are exposed to heat of direct sunlight.
- d. Solderability specified in **Clause 5.4.6** shall be guaranteed for 12 months from the date of delivery on condition that they are stored at the environment specified in **Clause 3**. For those parts, which passed more than 12 months shall be checked solder-ability before use.

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7. **Recommended Soldering Technologies** 7.1 Reflowing Profile:

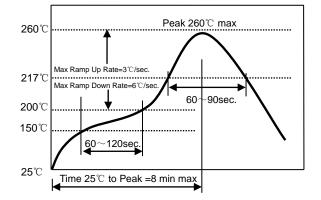
- Preheat condition: 150 ~200°C/60~120sec. \triangle
- Allowed time above 217°C: 60~90sec.
- \triangle
- \triangle Max temp: 260℃
- \triangle Max time at max temp: 10sec.
- Solder paste: Sn/3.0Ag/0.5Cu \triangle
- \triangle Allowed Reflow time: 2x max

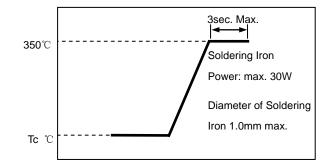
[Note: The reflow profile in the above table is only for qualification and is not meant to specify board assembly profiles. Actual board assembly profiles must be based on the customer's specific board design, solder paste and process, and should not exceed the parameters as the Reflow profile shows.]

7.2 Iron Soldering Profile.

- Iron soldering power: Max.30W \triangle
- \triangle Pre-heating: 150 °C / 60sec.
- \triangle Soldering Tip temperature: 350 °C Max.
- \triangle Soldering time: 3sec Max.
- \bigtriangleup Solder paste: Sn/3.0Ag/0.5Cu
- Max.1 times for iron soldering \triangle
- [Note: Take care not to apply the tip of

the soldering iron to the terminal electrodes.]





Appendix A: Electrical Characteristics

Part Number	L (µH)	L Test Freq. (MHz)	S.R.F Min. (MHz)	DCR (Typ) (Ω)	DCR (Max) (Ω)	Temperature Rise Current Irms (max.) (mA)	Saturation Current Isat(Typ.) (mA)	Saturation Current Isat(Max.) (mA)	Thickness (mm) [inch]
MPH201612S6R8□T	6.8	1	40	0.170	0.212	1200	220	180	1.1±0.1
MPH201612S100□T	10	1	35	0.250	0.312	1100	200	170	[.043±.004]

 $\%\,\square$: Please specify the inductance tolerance code (M=±20%, N=±30%).