SPECIFICATIONS

Customer						
Product Name		Mini Molded Chip Power Inductor				
Sunlord Part N	umber	мwтс	201208S Series	6		
Customer Part	Number					
[Xew Released, Revised]			SPEC No.: MW	TC0401230000		
【This SPEC is total 12 pages】 【ROHS, Compliant Parts】						
	Approved By	Checked By	Issued By			

Approved By	Checked By	Issued By

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[For Customer appro Qualification Status:		Date: estricted	ted
Approved By	Verified By	Re-checked By	Checked By
Comments:			
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【Version change history】

R	lev.	Effective Date	Changed Contents	Change Reasons	Approved By
(01	/	New release	1	Baizhi Liu

Caution

All products listed in this specification are developed, designed and intended for use in general electronics equipment. The products are not designed or warranted to meet the requirements of the applications listed below, whose performance and/or quality require especially high reliability, or whose failure, malfunction or trouble might directly cause damage to society, person, or property. Please understand that we are not responsible for any damage or liability caused by use of the products in any of the applications below. Please contact us for more details if you intend to use our products in the following applications.

- 1. Aircraft equipment
- 2. Aerospace equipment
- 3. Undersea equipment
- 4. nuclear control equipment
- 5. military equipment
- 6. Power plant equipment
- 7. Medical equipment
- 8. Transportation equipment (automobiles, trains, ships,etc.)
- 9. Traffic signal equipment
- 10. Disaster prevention / crime prevention equipment
- 11. Applications of similar complexity or with reliability requirements comparable to the applications listed in the above

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1. Scope

This specification applies to MWTC 201208S Series of Mini Molded Chip Power Inductor.

2. Product Description and Identification (Part Number)

- 1) Description
- MWTC 201208S Series of Mini Molded Chip Power Inductor. 2) Product Identification (Part Number)

MWTC	<u>201208</u>	<u>S</u>	<u>XXX</u>		<u>T</u>	<u>XXX</u>
1	2	3	(4)	(5)	6	$\overline{\mathcal{O}}$

① Type	
MWTC	Mini Molded Chip Power Inductor

③Feature Type	
S	Standard
U	Ultra Low Rdc
Н	High Saturation

©Inductance Tolerance				
Μ	±20%			
Ν	±30%			
6 Packing				
Т	Tape Carrier Package			

②External Dimensions (L x W xH) (mm)		
201208	2.0×1.2×0.8	

④Nominal Inductance	
Example	Nominal Value
R47	0.47µH
1R0	1.0µH

Internal Code			
D	Design code		
Р	Process code		
* Conventional product is blank			

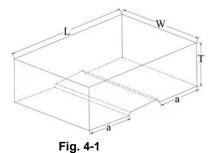
3. Electrical Characteristics

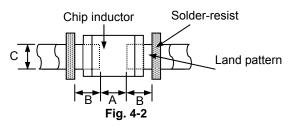
Please refer to Appendix A (Page 11).

- 1) Operating and storage temperature range (individual chip without packing): -40 $^\circ$ C ~ +125 $^\circ$ C
- 2) Storage temperature range (packaging conditions): -10°C~+40°C and RH 70% (Max.)

4. Shape and Dimensions

- 1) Dimensions and recommended PCB pattern for reflow soldering: See Fig.4-1, Fig.4-2 and Table 4-1.
- 2) Structure: See Fig. 4-3 and Fig. 4-4.

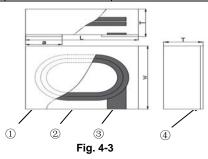


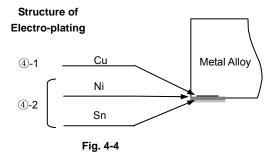


[Table 4-1]

Unit: mm [inch]

Туре	L	W	т	а	А	В	С	
201208	2.0 ±0.2 [.079 ±.008]	1.2±0.2 [.047±.008]	0.8MAX [.031MAX]	0.6±0.2 [.024±.008]	0.7 Тур	0.8 Тур	1.2 Тур	





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3) Material Information: See Table 4-2.

[Table 4-2]

Code	Part Name	Material Name				
1	Metal Alloy Body	Metal Alloy Powder				
2	Inner Coils	Cu Coil				
3	Pull-out Electrode (Cu)	Cu				
④-1	Terminal Electrode: Inside Cu	Plating Chemicals				
④-2	Electro-Plating: Ni/Sn plating	Plating Chemicals				

5. Test and Measurement Procedures

5.1 Test Conditions

Unless otherwise specified, the standard atmospheric conditions for measurement/test as:

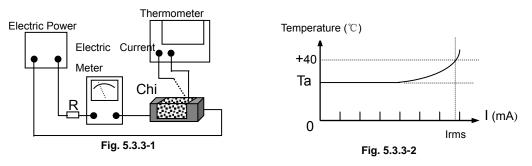
- a. Ambient Temperature: 20±15°C
- b. Relative Humidity: 65±20%
- c. Air Pressure: 86kPa to 106kPa
- If any doubt on the results, measurements/tests should be made within the following limits:
 - a. Ambient Temperature: 20±2°C
 - b. Relative Humidity: 65±5%
 - c. Air Pressure: 86kPa to 106kPa

5.2 Visual Examination

a. Inspection Equipment: 20× magnifier

5.3 Electrical Test

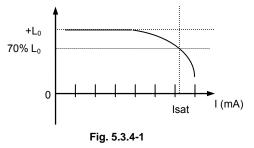
- 5.3.1 DC Resistance (DCR)
 - a. Refer to Electrical Characteristics.
 - b. Test equipment (Analyzer): High Accuracy Milliohmmeter-HIOKI RM3544 or equivalent.
- 5.3.2 Inductance (L)
 - a. Refer to Electrical Characteristics.
 - b. Test equipment: High Accuracy RF Impedance /Material Analyzer-WK 3260B or equivalent.
 - c. Test signal:1V.
 - d. Test frequency refers to Electrical Characteristics.
- 5.3.3 Temperature Rise Current (Irms)
 - a. Refer to Electrical Characteristics.
 - b. Test equipment (see Fig. 5.3.3-1): Electric Power, Electric current meter, Thermometer.
 - c. Measurement method (see Fig. 5.3.3-1):
 - 1. Set test current to be 0 mA.
 - 2. Measure initial temperature of chip surface.
 - 3. Gradually increase voltage and measure chip temperature for corresponding current.
 - 4. Definition of Temperature Rise Current (Irms) : Irms is direct electric current as chip surface temperature rose just 40°C against chip initial surface temperature (Ta) (see Fig. 5.3.3-2)



5.3.4 Saturation Current (Isat)

- a. Refer to Electrical Characteristics.
- b. Test equipment: High Accuracy RF Impedance /Material Analyzer- WK 3260B or equivalent.
- c. Measurement method:
 - Measurement conditions of initial inductance L: Measuring Frequency: 1MHz. Test Current: 1mA.

2. Definition of Saturation Current (Isat) : Isat is the value of DC current as inductance L (µH) decreased just 30% against initial value (see Fig. 5.3.4-1).



5.3.5 Self-Resonant Frequency (SRF)

- a. Refer to Electrical Characteristics.
- b. Test equipment: High Accuracy RF Impedance /Material Analyzer--WK 3260B or equivalent.
- c. Test signal: 1V.

5.4 Reliability Test

Items	Requirements	Test Methods and Remarks				
5.4.1 Terminal Strength	No removal or split of the termination or other defects shall occur. Chip Chip Glass Epoxy Board Fig.5.4.1-1	 Solder the inductor to the testing jig (glass epoxy board shown in Fig.5.4.1-1) using eutectic solder. Then apply a 10N force in the direction of the arrow. Keep time: 10±1s. Speed: 1.0mm/s. 				
5.4.2 Resistance to Flexure	No visible mechanical damage. Unit: mm [inch] Type a b c 201208 0.8 2.4 1.4 07 07 100 Fig.5.4.2-1	 Solder the inductor to the test jig (glass epoxy board shown in Fig.5.4.2-1) Using a eutectic solder. Then apply a force in the direction shown Fig. 5.4.2-2. Flexure: 2mm. Pressurizing Speed: 0.5mm/sec. Keep time: 30 sec. Test board size: 100X40X1.0. 				
5.4.3 Vibration	 No visible mechanical damage. Inductance change: Within ±10%. Cu pad Solder mask Cu pad Fig. 5.4.3-1 	 Solder the inductor to the testing jig (glass epoxy board shown in Fig.5.4.3-1) using eutectic solder. The inductor shall be subjected to a simple harmonic motion having total amplitude of 1.5mm, the frequency being varied uniformly between the approximate limits of 10 and 55 Hz. The frequency range from 10 to 55 Hz and return to 10 Hz shall be traversed in approximately 1 minute. This motion shall be applied for a period of 2 hours in each 3mutually perpendicular directions (total of 6 hours). 				
5.4.4 Dropping 5.4.5 Temperature 5.4.6 Solderability	 No visible mechanical damage. Inductance change: Within ±10%. Inductance change should be within ±20% of initial value measuring at 25 °C. No visible mechanical damage. Wetting shall exceed 90% coverage. 	Drop chip inductor 10 times on a concrete floor from a height of 100 cm. Temperature range: -40°C~ +125°C Reference temperature: +25°C ① Solder temperature: 245±2°C ② Duration: 3 sec. ③ Solder: Sn/3.0Ag/0.5Cu. ④ Flux: 25% Resin and 75% ethanol in weight.				

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· · · · · · · · · · · · · · · · · · ·	less categories: Level 0 (general confidential) Specifica					
5.4.7 Resistance to	 No visible mechanical damage. Inductance change: Within ±10%. 	Re-flowing Profile: Please refer to Fig. 5.4.7-1 . Toot board thickness: 1 0mm				
	2 Inductance change: Within ±10%.	2 Test board thickness: 1.0mm				
Soldering Heat		 ③ Test board material: glass epoxy resin ④ The chip shall be stabilized at normal condition for 1~2 				
		hours before measuring				
		260°C 217°C Max Ramp Up Rate=3°C/sec. 200°C 150°C <u>60</u> ~120sec				
		25℃ Time 25℃ to Peak =8 min max				
5.4.8	 No mechanical damage. 	① Temperature, Time: (See Fig.5.4.8-1)				
5.4.6 Thermal Shock	 2 Inductance change: Within ±10%. 	-40°C for 30±3 min \rightarrow 125°C for 30±3min.				
		 2 Transforming interval: 20 sec.(max.). 				
	30 min. 30 min.	③ Tested cycle: 100 cycles.				
	125°C	 The chip shall be stabilized at normal condition for 1~2 				
	Ambient	hours before measuring.				
	Temperature 30 min.					
	-40 ℃ 20sec. (max.)					
	Fig. 5.4.8-1					
5.4.9 Resistance	① No mechanical damage.	① Temperature: -40±2℃				
to Low	 2 Inductance change: Within ±10%. 	2 Duration: 1000^{+24} hours.				
Temperature		 3 The chip shall be stabilized at normal condition for 1~2 				
		hours before measuring.				
5.4.10	① No mechanical damage.	① Temperature: 125±2℃				
Resistance to	 2 Inductance change: Within ±10%. 	2 Duration: 1000^{+24} hours.				
High		 Buildin root notify The chip shall be stabilized at normal condition for 1~2 				
Temperature		hours before measuring.				
5.4.11	① No visible mechanical damage.	① Temperature: 60±2°C				
Damp Heat	② Inductance change: Within ±10%.	2 Humidity: 90% to 95% RH.				
(Steady States)		③ Duration: 1000 ⁺²⁴ hours.				
		④ The chip shall be stabilized at normal condition for 1~2				
E 4 10	1. No visible mechanical demose	hours before measuring.				
5.4.12	 No visible mechanical damage. Inductance change: Within ±10%. 	① Temperature: 60±2℃				
Loading Under		② Humidity: 90% to 95% RH.				
Damp Heat		③ Duration: 1000+24 hours.				
		④ Applied current: Rated current.				
		5 The chip shall be stabilized at normal condition for 1~2 hours				
		before measuring.				
5.4.13	① No visible mechanical damage.	① Temperature: 85±2°C				
Loading at High	② Inductance change: Within ±10%.	 Duration: 1000+24 hours. 				
Temperature		③ Applied current: Rated current.				
(Life Test)		 ④ The chip shall be stabilized at normal condition for 1~2 hours 				
(
		before measuring.				

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6. Packaging and Storage

6.1 Packaging

Tape Carrier Packaging:

Packaging code: T

- a. Tape carrier packaging are specified in attached figure Fig.6.1-1~4
- b. Tape carrier packaging quantity please see the following table:

Туре	201208					
T(mm)	0.8MAX					
Таре	Embossed Tape					
Quantity	ЗK					

- c. Reel shall be packaged in vinyl bag.
- d. Maximum of 5 or 10 reels bags shall be packaged in an inner box.
- e. Maximum of 6 or 10 inner boxes shall be packaged in an outer case.
- (1) Taping Drawings (Unit: mm)

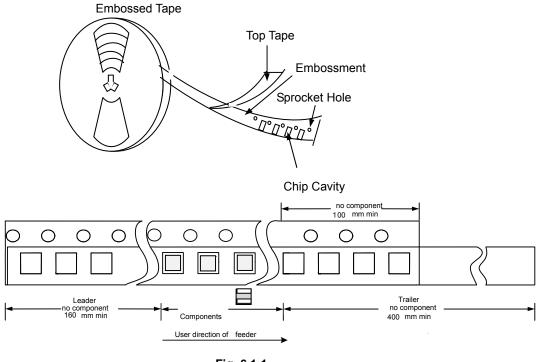
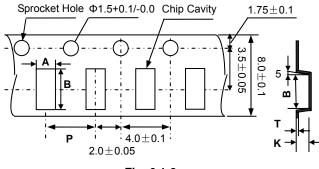


Fig. 6.1-1

Remark: The sprocket holes are to the right as the tape is pulled toward the user.

(2) Taping Dimensions (Unit: mm)

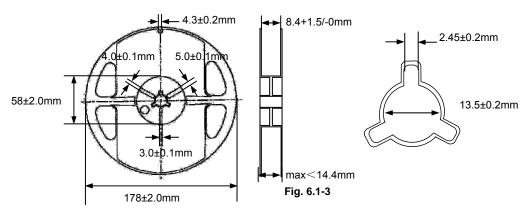


 Type
 A
 B
 P
 Kmax
 Tmax

 MWTC201208
 1.50±0.1
 2.30±0.1
 4.0±0.1
 1.1
 0.3

Fig. 6.1-2

(3) Reel Dimensions (Unit: mm)



6.2 Storage

a To maintain the solderability of terminal electrodes and to keep the packing material in good condition, temperature and humidity in the storage area should be controlled.

b. Recommended conditions: -10°C~40°C, 70%RH (Max.)

c. Even under ideal storage conditions, solderability of products electrodes may decrease as time passes. For this reason, product

should be used with one year from the time of delivery.

d. In case of storage over 6 months, solderability shall be checked before actual usage.

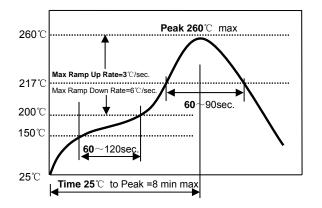
File No: Effective date:		Applied to asser	REV: 01	
No. Defect Item		Graphic	Acceptance	
1	Core defect		Rejection identification The defect length and width (L and W) more than L/4 and W/4, NG.	AQL=0.65
2	Tin point	•	 Electrode surface: 1. The climbing length of tin is greater than 1/4 of the electrode spacing,NG. Print literally: 1. The length and width (L and W) of tin point is greater than 0.25 mm and the quantity is more than 8, NG. 2. The length and width of tin dots are less than 0.05mm, which is not considered. Side: 1. The climbing height of tin is greater than 1/2 of the product height, NG. 	AQL=0.65

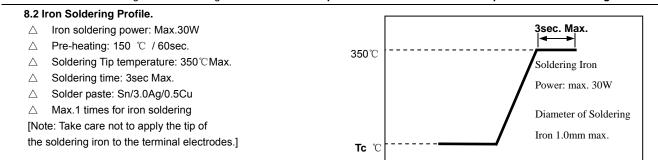
7. Visual inspection standard of product

8. Recommended Soldering Technologies 8.1 Reflowing Profile:

- \triangle Preheat condition: 150 ~200 °C/60~120sec.
- \triangle Allowed time above 217°C: 60~90sec.
- △ Max temp: 260°C
- \triangle Max time at max temp: 10sec.
- △ Solder paste: Sn/3.0Ag/0.5Cu
- \triangle Allowed Reflow time: 2x max

[Note: The reflow profile in the above table is only for qualification and is not meant to specify board assembly profiles. Actual board assembly profiles must be based on the customer's specific board design, solder paste and process, and should not exceed the parameters as the Reflow profile shows.]





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Annondix A: Electrical Characteristics									

Electrical CharacteristicsPart Number	L	L Test Freq.	S.R.F Min.	DC Resistance		Saturation Current (Isat)		Temperature Rise Current (Irms)		Thickness
				Тур.	Max.	Тур.	Max.	Тур.	Max.	
Unit	μH	MHz	MHz	mΩ	mΩ	А	А	А	А	mm [inch]
MWTC201208SR11□T	0.11	1	185	10	13	11	10	6.5	5.6	0.8MAX [.039MAX]
MWTC201208SR24□T	0.24	1	130	16	19	7.2	6.5	6.0	5.4	0.8MAX [.039MAX]
MWTC201208SR33□T	0.33	1	125	23	28	6.2	5.6	4.3	4.0	0.8MAX [.039MAX]
MWTC201208SR47□T	0.47	1	96	37	42	6.2	5.5	3.9	3.7	0.8MAX [.039MAX]
MWTC201208SR47 TD01	0.47	1	96	22	25	4.8	4.5	4.4	4.0	0.8MAX [.039MAX]
MWTC201208S1R0□T	1.0	1	74	92	102	3.1	2.8	2.3	2.0	0.8MAX [.039MAX]
MWTC201208S1R0□TD01	1.0	1	60	46	50	3.5	3.3	3.5	3.2	0.8MAX [.039MAX]
MWTC201208S2R2□T	2.2	1	45	216	238	2.5	2.2	1.3	1.1	0.8MAX [.039MAX]
MWTC201208S2R2□TD01	2.2	1	42	120	130	2.1	1.9	2.0	1.8	0.8MAX [.039MAX]

 \square : Please specify the inductance tolerance code (M=±20%, N=±30%).

※:Rated current: Isat or Irms, whichever is smaller.

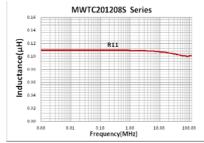
%: Saturation Current: Max. Value, DC current at which the inductance drops less than 30% from its value without current; Typ. Value, DC current at which the inductance drops 30% from its value without current.

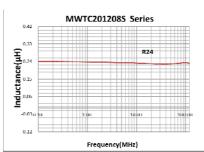
*: Temperature Rise Current: DC current that causes the temperature rise (ΔT) from 20°C ambient.

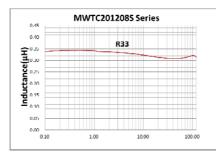
For Max. Value, $\Delta T {<} 40^{\circ}$ C; for Typ. Value, ΔT is approximate 40° C.

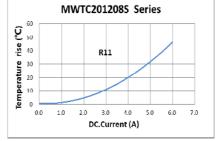
:Rated voltage: 20V.

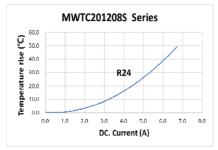
Inductance vs. Frequency Characteristics Temperature vs. DC Current Characteristics Inductance vs. DC Current Characteristics

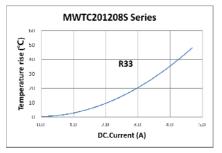


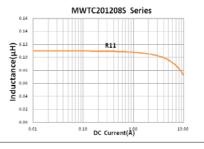


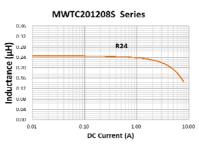


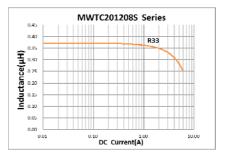




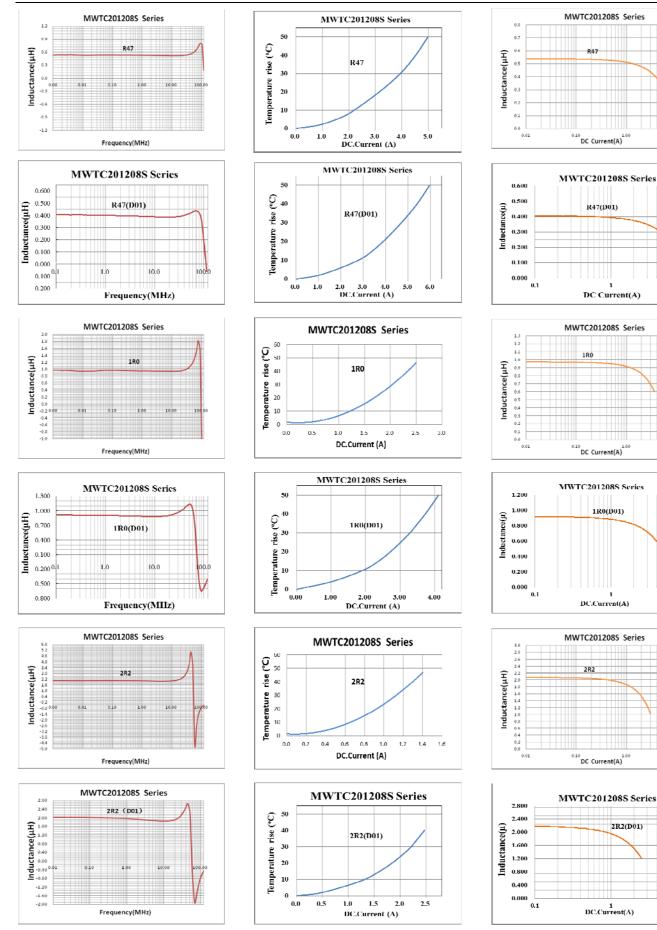








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